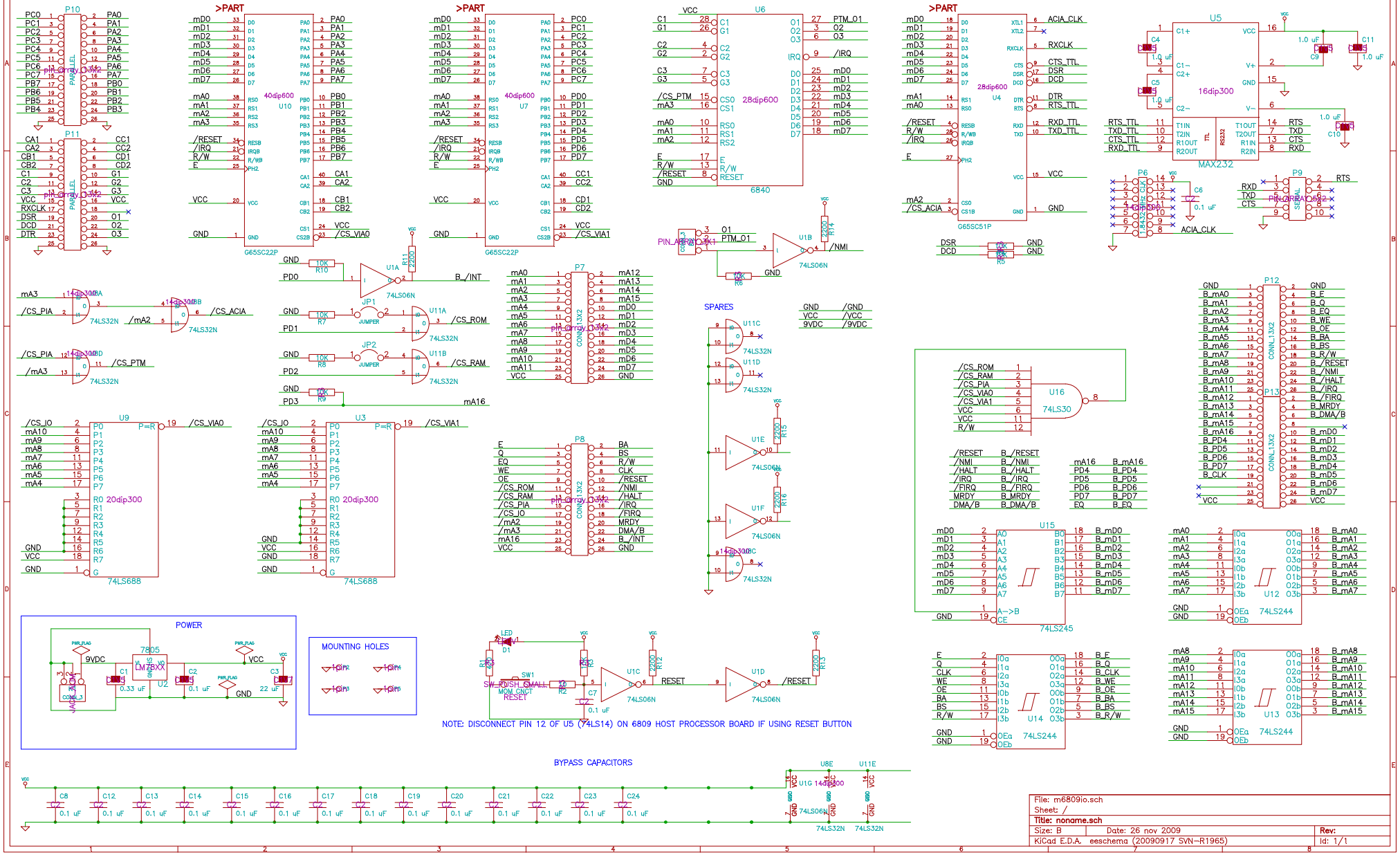
P10
PARALLEL
P11
PARALLEL
U10
40dip600
G65SC22P
U7
40dip600
G65SC22P
U6
6840
28dip600
U4
G65SC51P
U5
16dip300
MAX232
P6
ACIA_CLK
P9
SERIAL
U1A
74LS06N
U1B
74LS06N
U11A
74LS32N
U11B
74LS32N
U11C
74LS32N
U11D
74LS32N
U1E
74LS06N
U1F
74LS06N
U1C
74LS06N
U1D
74LS06N
U8E
U11E
U1G
74LS06N
74LS32N
74LS32N
P7
CONN_13X2
P8
CONN_13X2
P12
CONN_13X2
P13
CONN_13X2
U9
74LS688
U3
74LS688
U16
74LS30
U15
74LS245
U14
74LS244
U12
74LS244
U13
74LS244
SPARES
POWER
MOUNTING HOLES
BYPASS CAPACITORS
NOTE: DISCONNECT PIN 12 OF U5 (74LS14) ON 6809 HOST PROCESSOR BOARD IF USING RESET BUTTON
File: m6809io.sch
Sheet: /
Title: noname.sch
Size: B
Date: 26 nov 2009
Rev:
KiCad E.D.A. eeschema (20090917 SVN-R1965)
Id: 1/1